



AF ZPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 5320

USHIYAMA et al.

Examiner: T. Le

Serial No.: 10/694,997

Art Unit: 2818

Filed: October 29, 2003

Docket No. 100353-00176

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING REDUCED
CROSS-TALK NOISE

RESPONSE UNDER 37 C.F.R. § 1.116

BOX AF

June 15, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

*please enter papers dated
6/15/05*

Sir:

Thao Le

This paper is filed in response to the Office Action mailed March 17, 2005, in
the above-captioned patent application. In the prosecution of the above-captioned
patent application, please consider the following: